

University of Hyderabad
Central University P.O., Hyderabad – 500046
Centre for Advanced Studies in Electronics Science and technology (CASEST), School of Physics

Schedule: Interview for admission to Ph.D. Electronics Science and Engineering

Date of interview : 11/02/2021 (Thursday)

Instructions:

- 1) Student should login to the waiting room by the specified time (See the schedule below).
- 2) A google meet link for “waiting room” will be sent to the candidates by e-mail.
- 3) Detailed instructions will be sent to the candidates by e-mail.

Schedule:

S. No.	H.T. No.	Name	Interview Time slot	Time to enter online in the waiting room
1	15068045	R VINAY KUMAR BANDILA	10:00 AM – 10:25 AM	09:30 AM
2	15068001	AILA MRUDHULA	10:30 AM – 10:55 AM	
3	15068004	BANKA RAJU	11:00 AM – 11:25 AM	
4	15068053	SRUJANA CH	11:30 PM – 11:55 PM	
5	15068058	SURAJ KUMAR VERMA	12:00 Noon – 12:25 PM	
6	17068004	JAY DIP MATHUR	12:30 PM – 12:55 PM	
7	15068007	CHAMARTHI SAI DURGA KAUMUDI	02:00 PM – 02:25 PM	01:30 PM
8	15068035	MERUGA UDAYA	02:30 PM – 02:55 PM	
9	15068037	MOHANA KALAHASTHI	03:00 PM – 03:25 PM	
10	18068001	ARUP KUNDU CHOWDHURY	03:30 PM – 03:55 PM	

Head, CASEST