

UNIVERSITY OF HYDERABAD
Central University P.O., Hyderabad - 500 046

O/o. the Controller of Examinations
Academic Section
Dated: 01-Feb-2016

No.UH/Acad/EE/2015/

Visvesvaraya Ph.D. Scheme - 2015

Revised Admission Schedule

Sub: Admissions 2015-2016- Revised Schedule for admission to **Ph.D. Computer Science / Electronics Science.**

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In continuation to the notification dated 06-01-2016 and 18-01-2016, for admission to Ph.D. Computer Science and Electronics Science, the main list candidates are required to report for admission at 9.00 a.m. on 11th February, 2016 and the wait listed candidates have to report at 9.00 a.m. on 12th February, 2016 at the counseling hall of the Office of the Controller of Examinations.

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Controller of Examinations